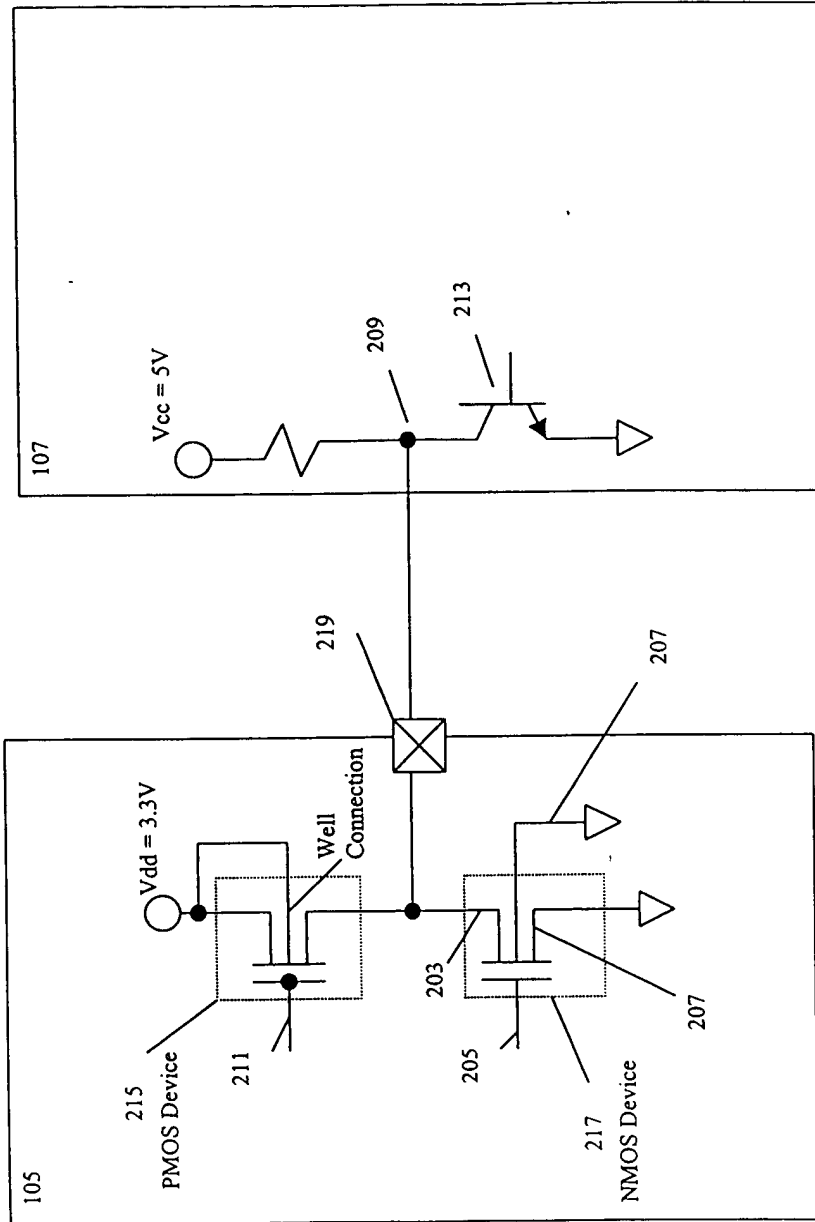


FIGURE 1



Prior Art

Figure 2

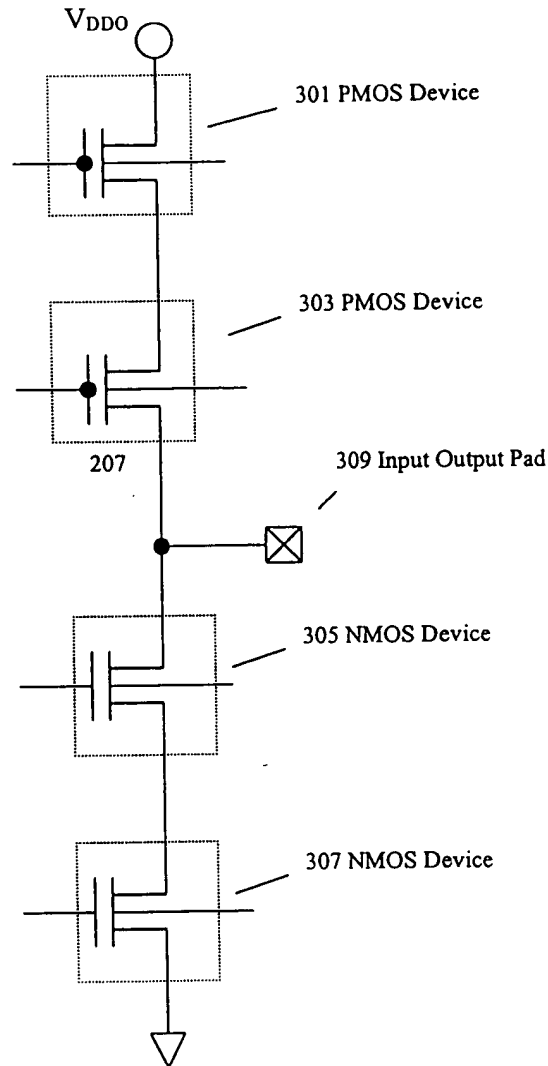


Figure 3

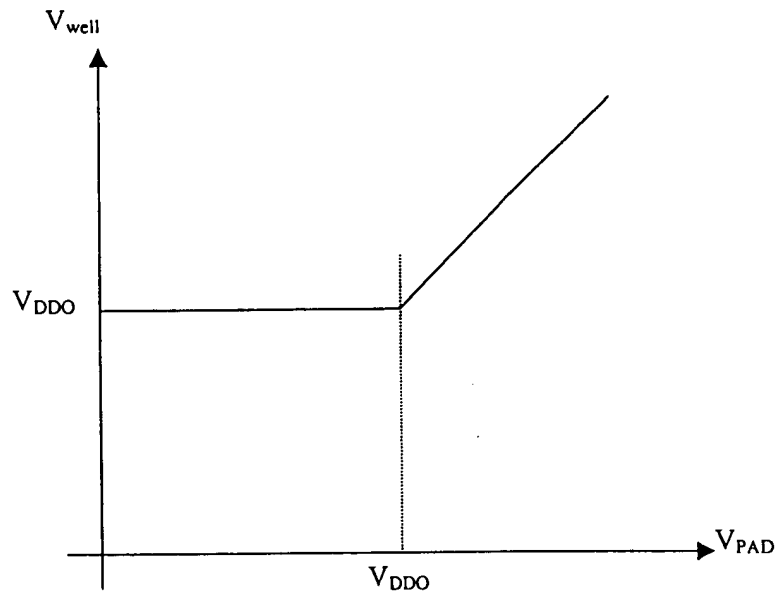


Figure 5

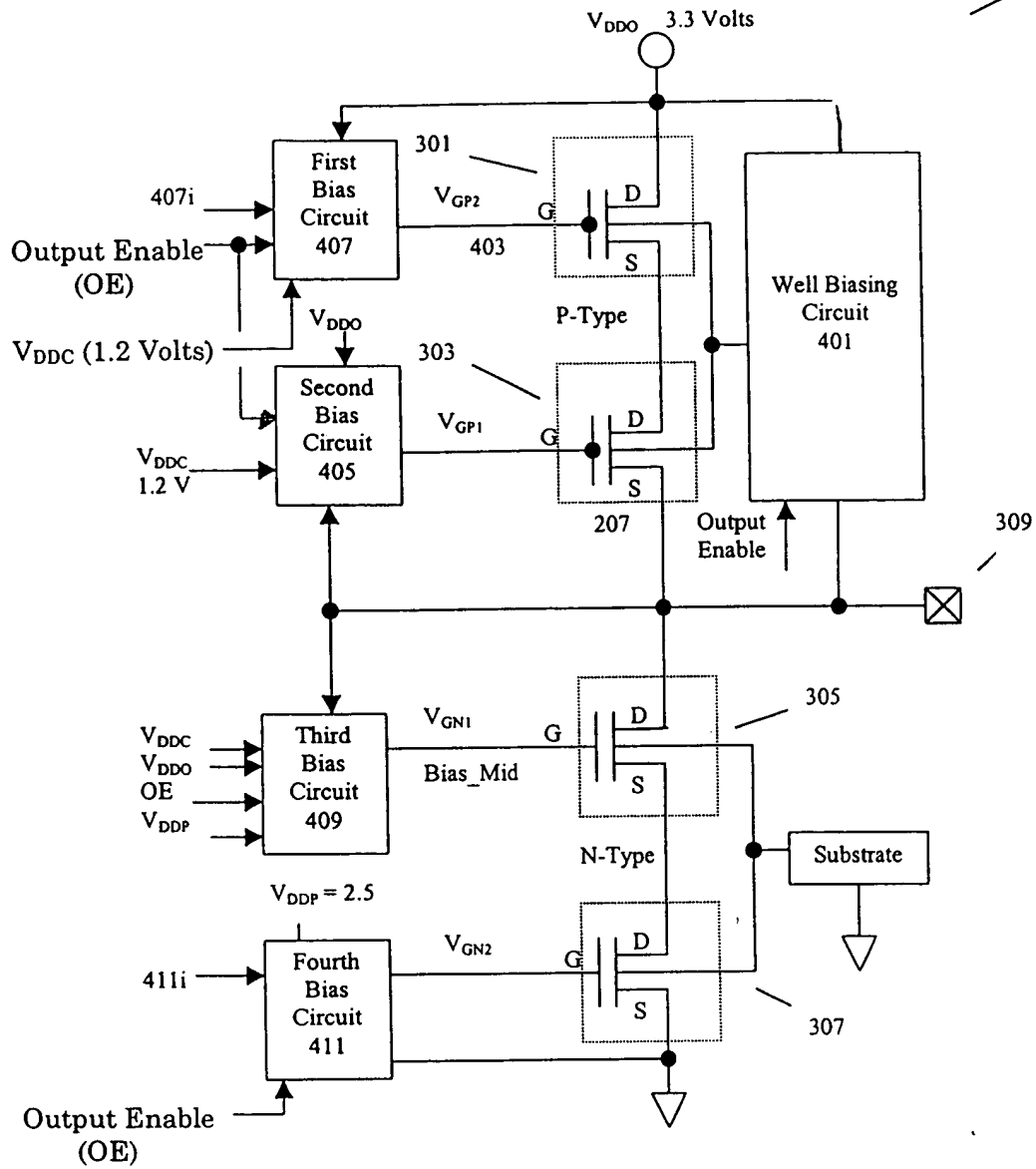


Figure 6

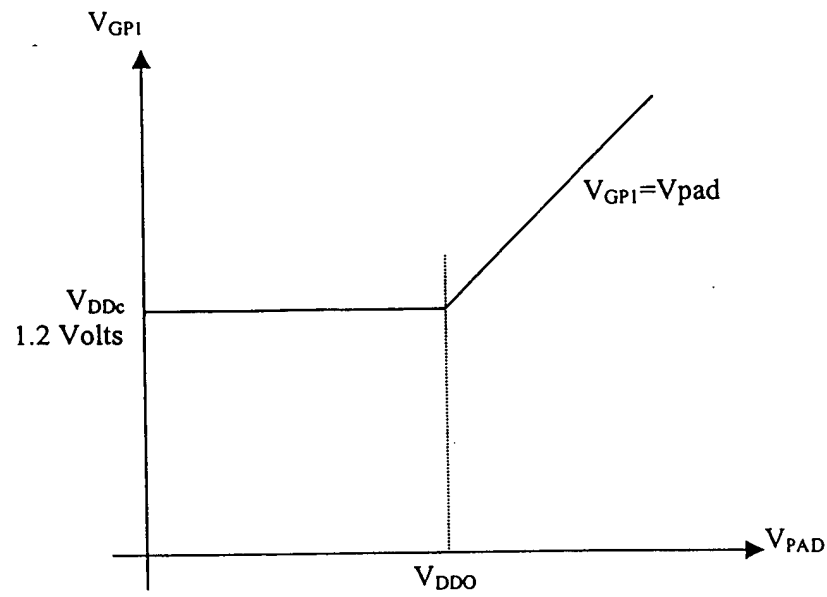


Figure 7

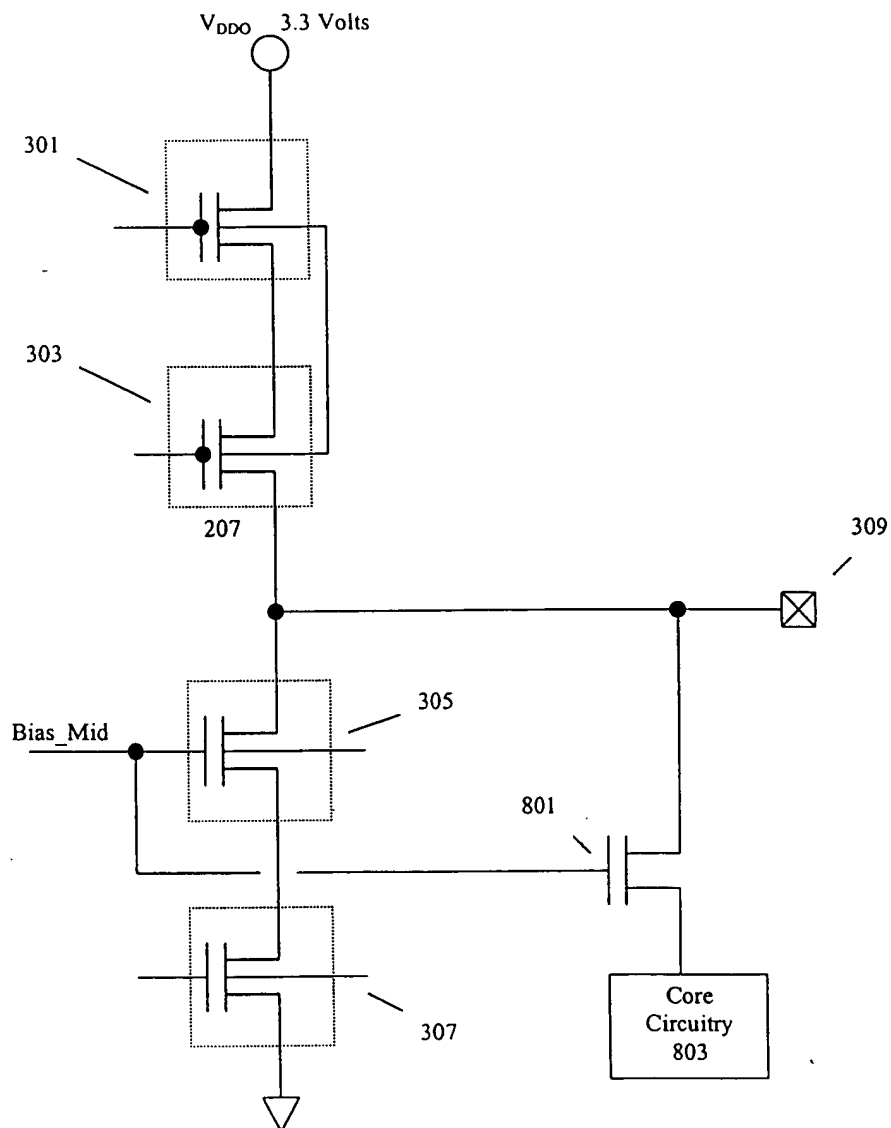
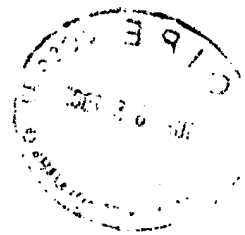


Figure 8

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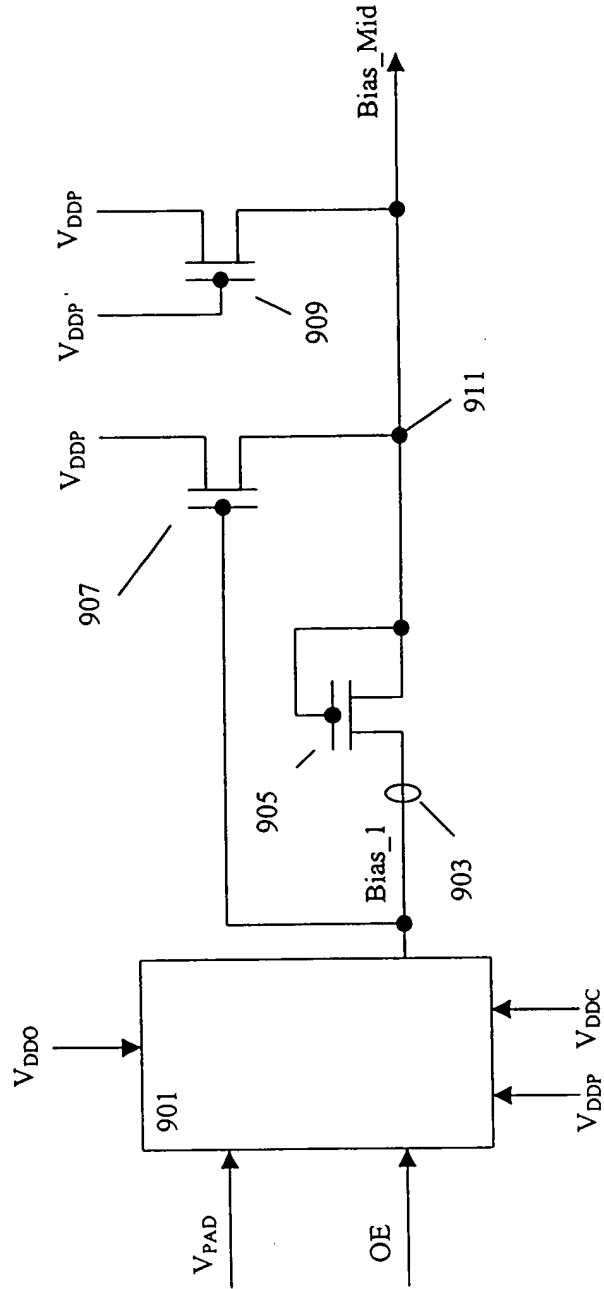


Figure 9A

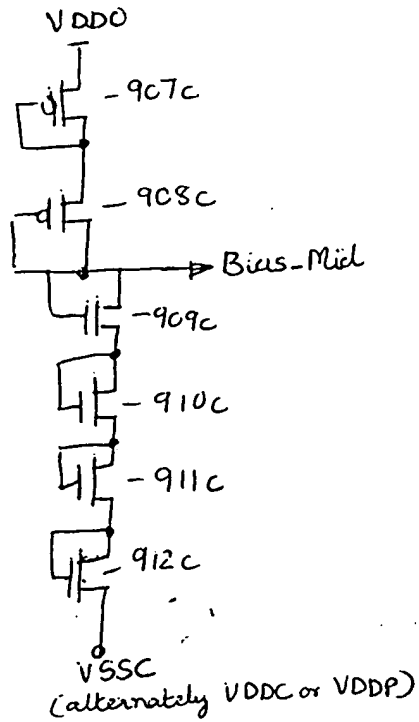


Figure 9B

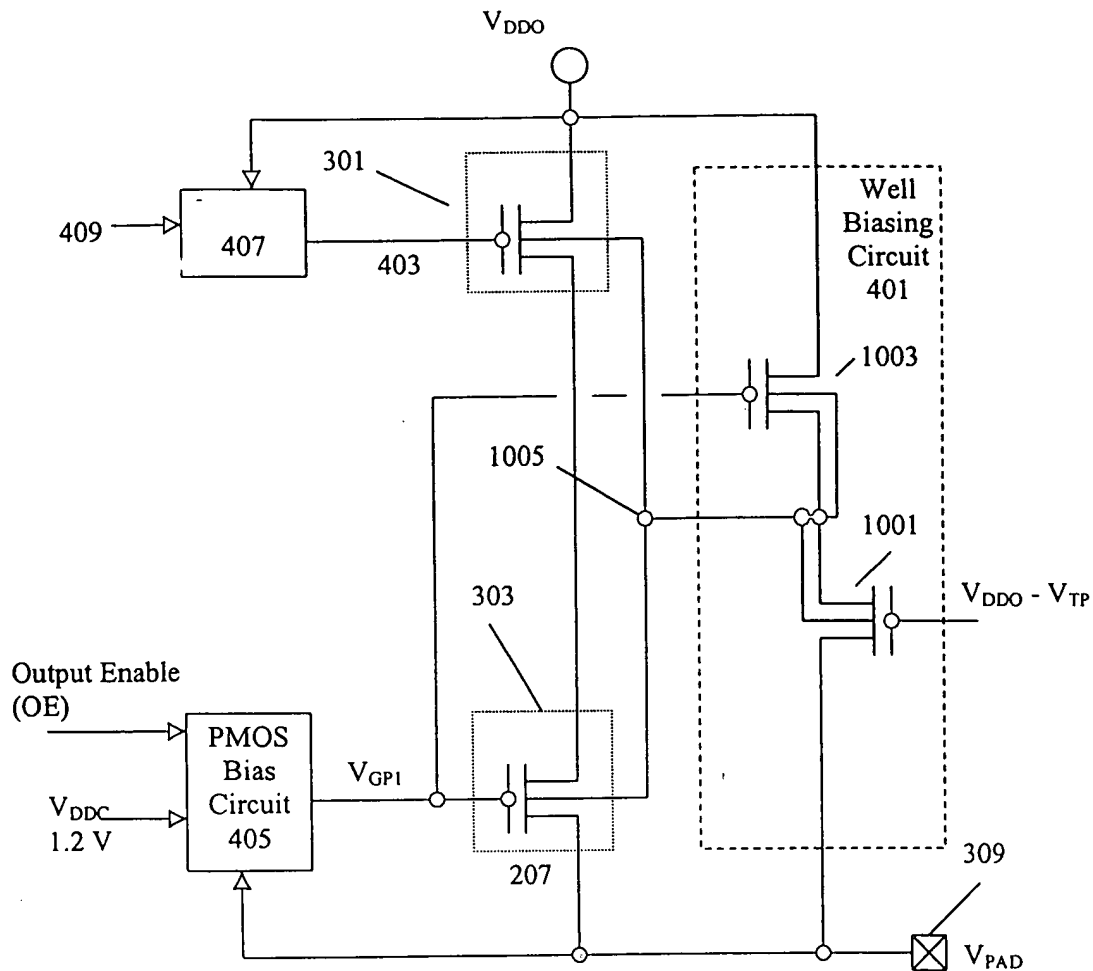


Figure 10

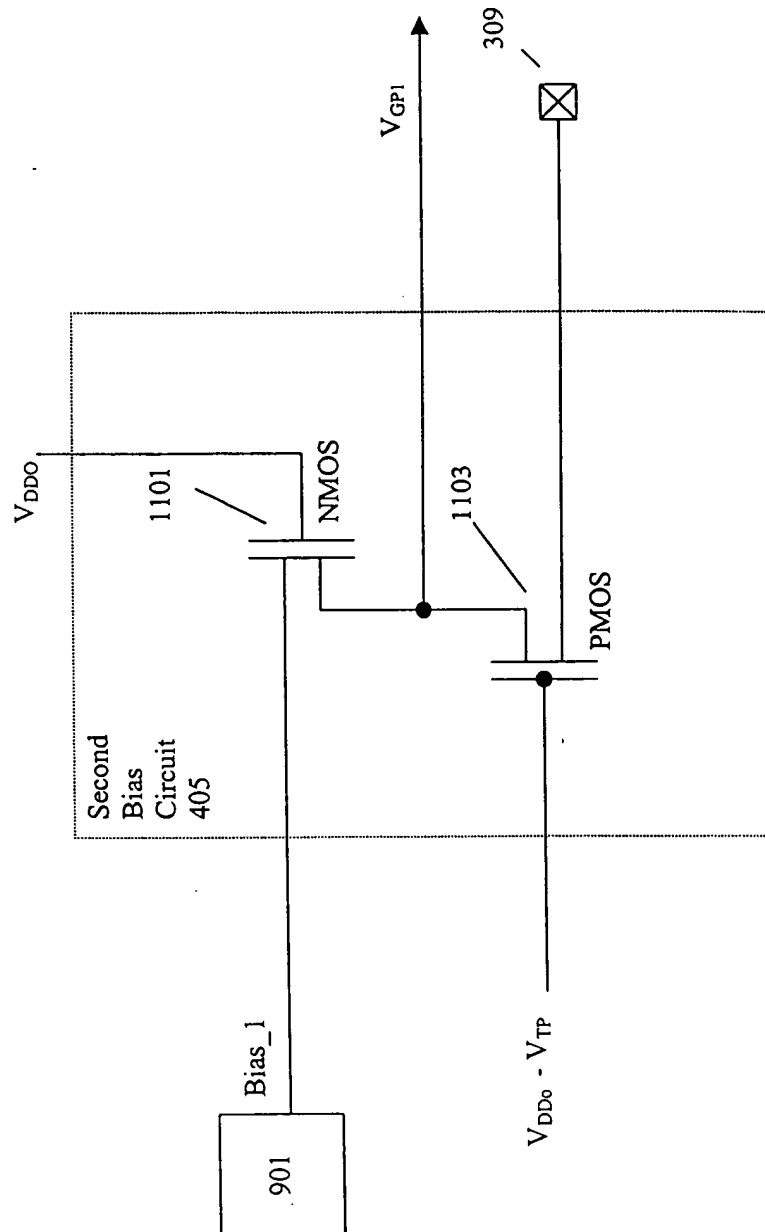


Figure 11A

NO. 10/043,763
SUB-MICRON HIGH INPUT VOLTAGE
TOLERANT INPUT OUTPUT (I/O) CIRCUIT
WHICH ACCOMMODATES LARGE POWER
SUPPLY VARIATIONS

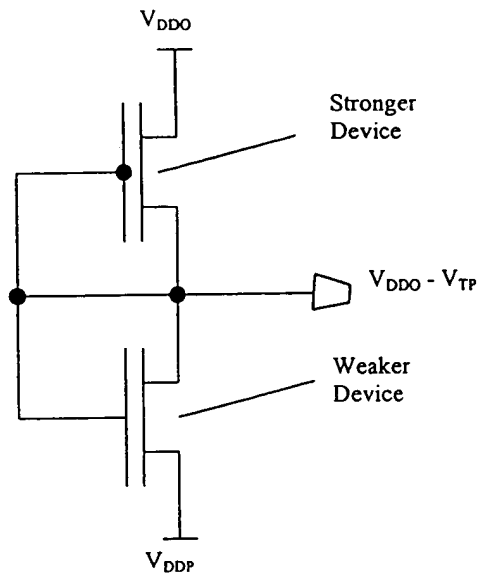
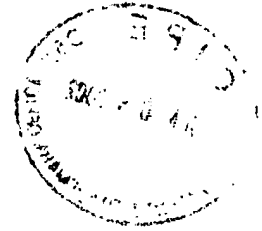


Figure 11B

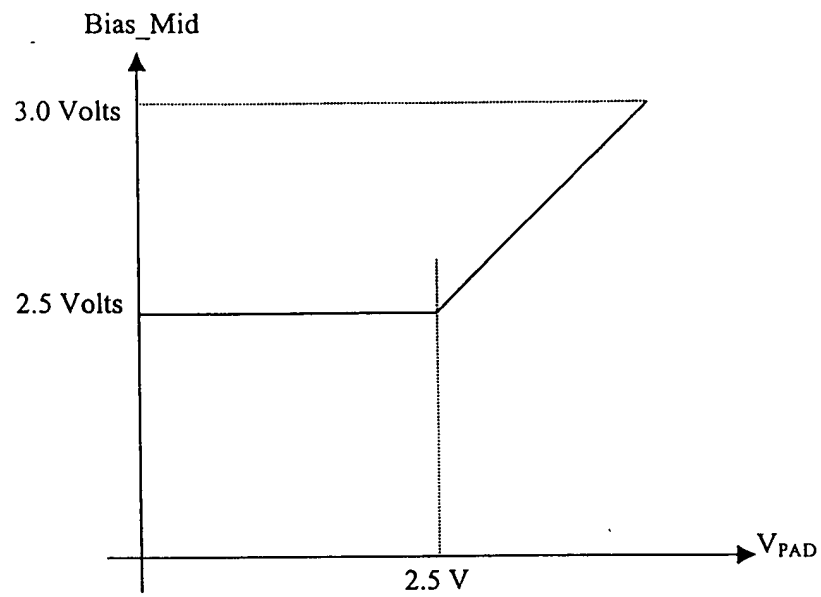


Figure 11C

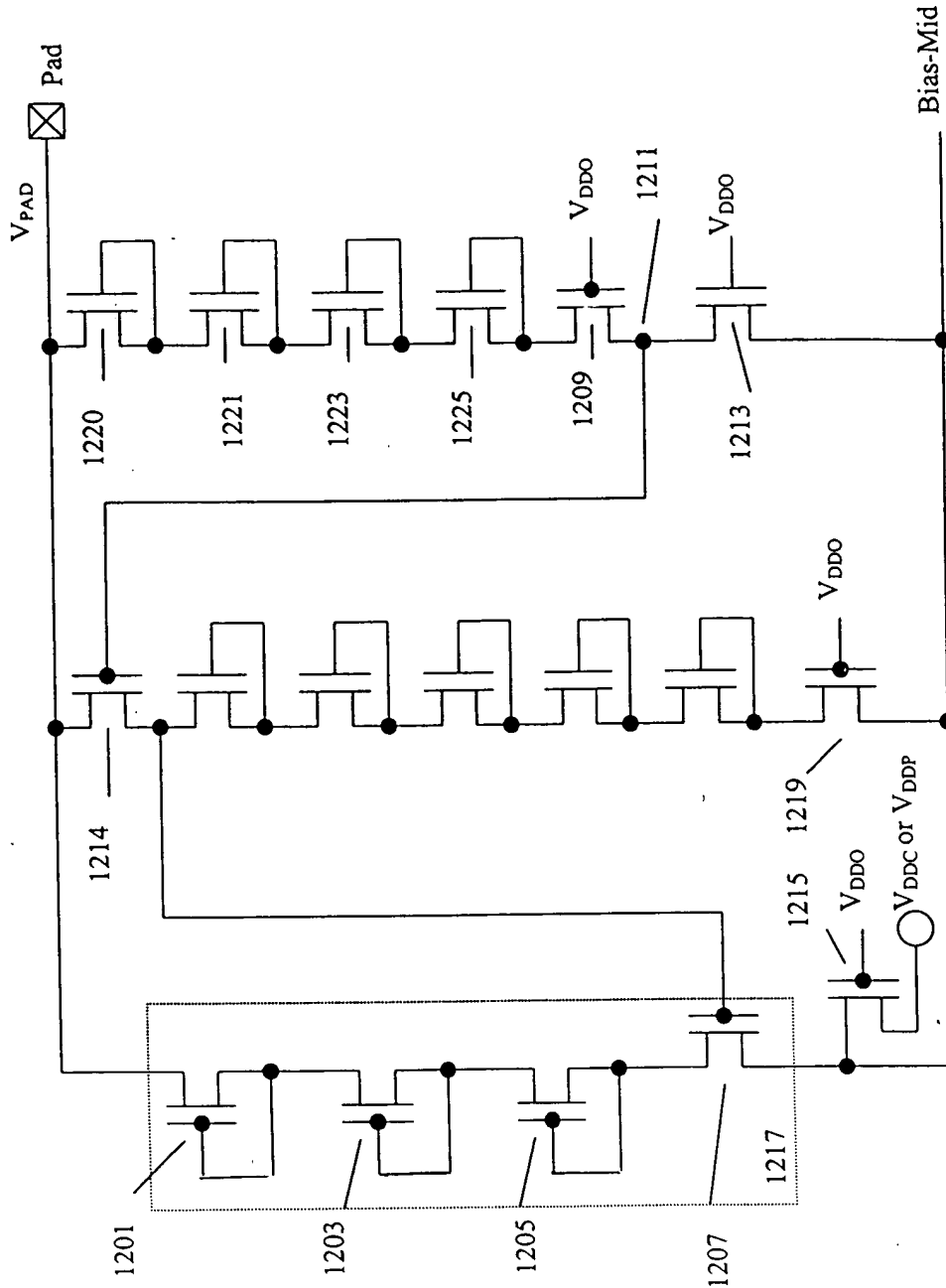


Figure 12 A

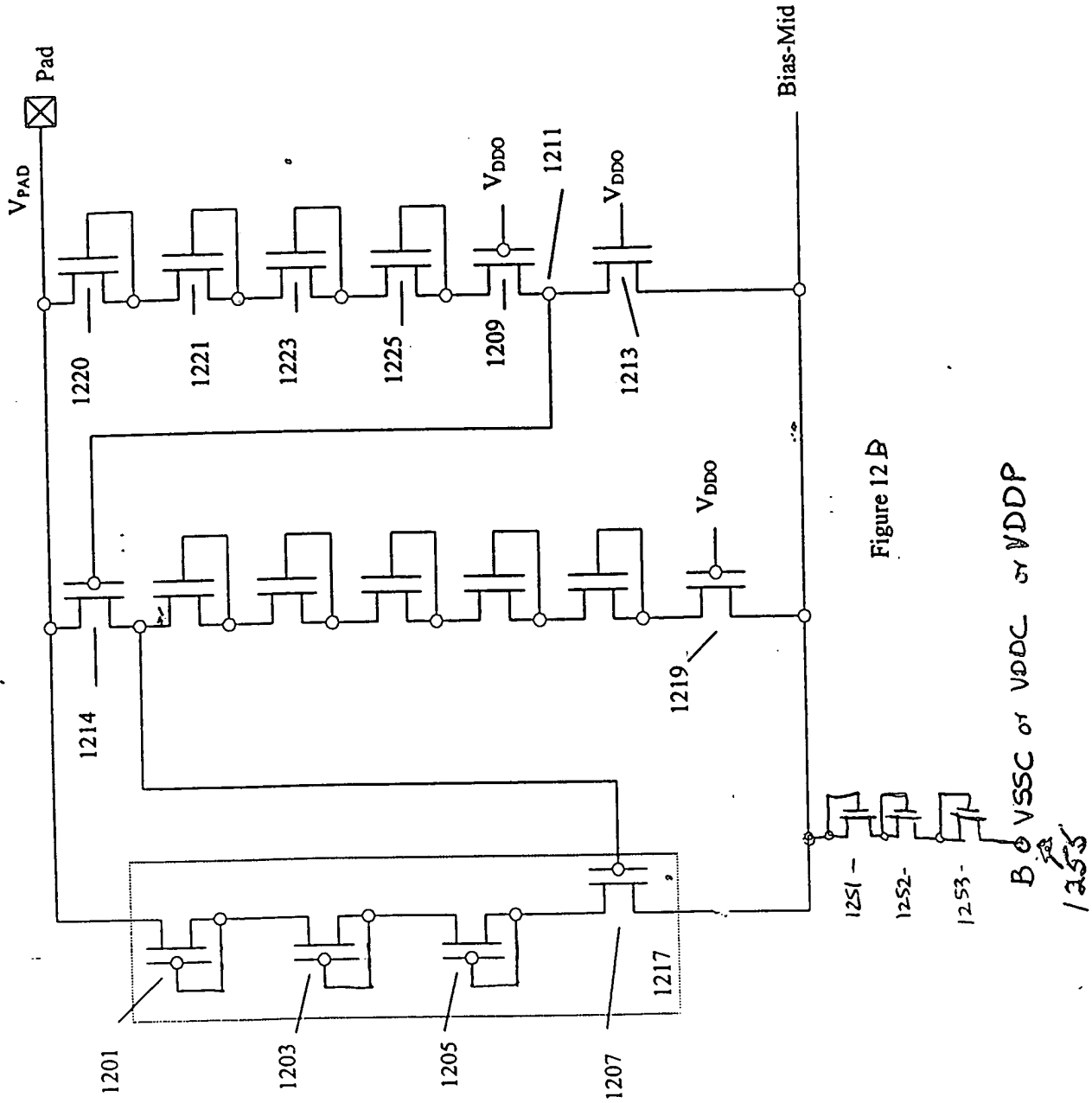
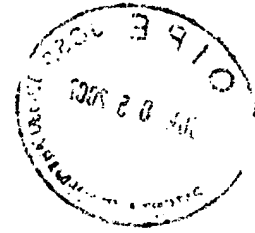


Figure 12 B

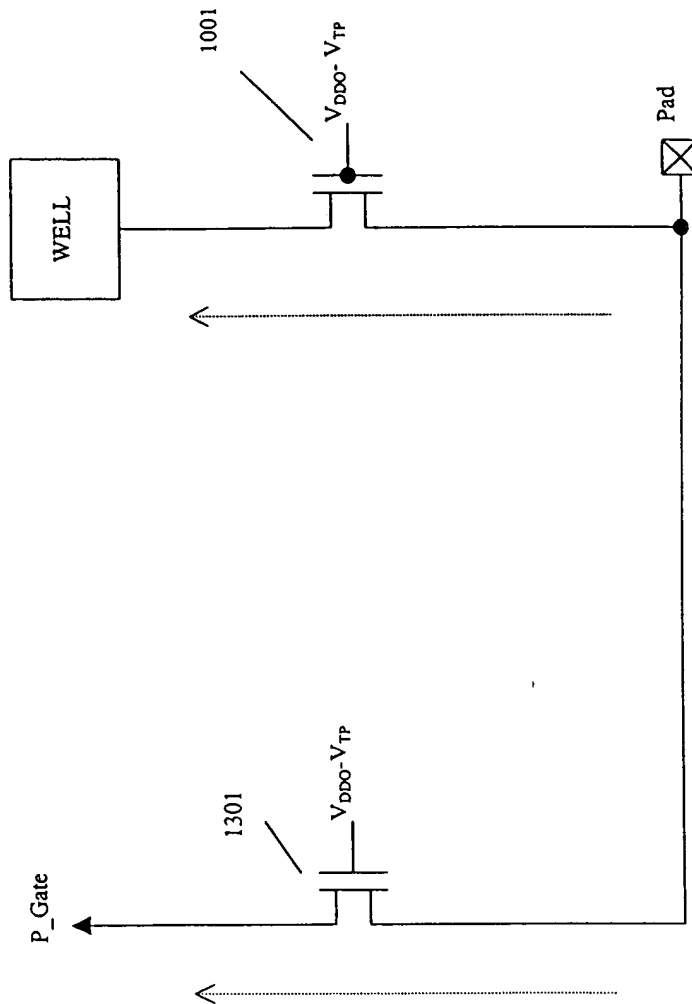


Figure 13

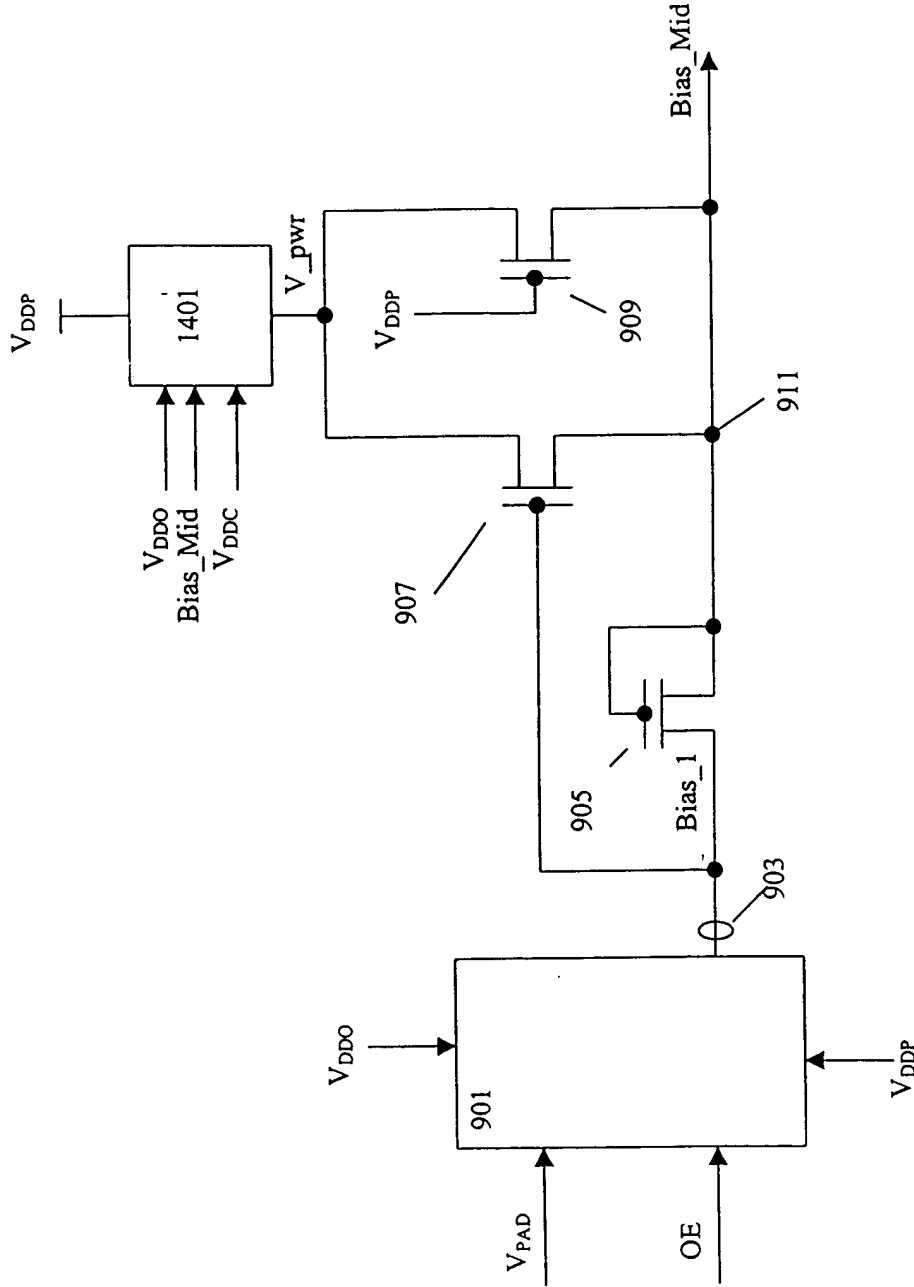


Figure 14

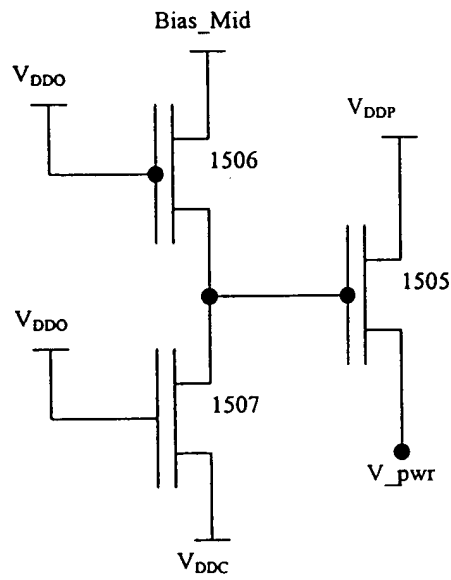
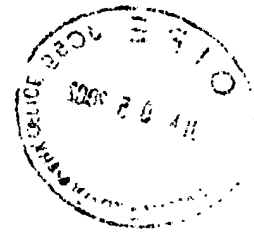


Figure 15

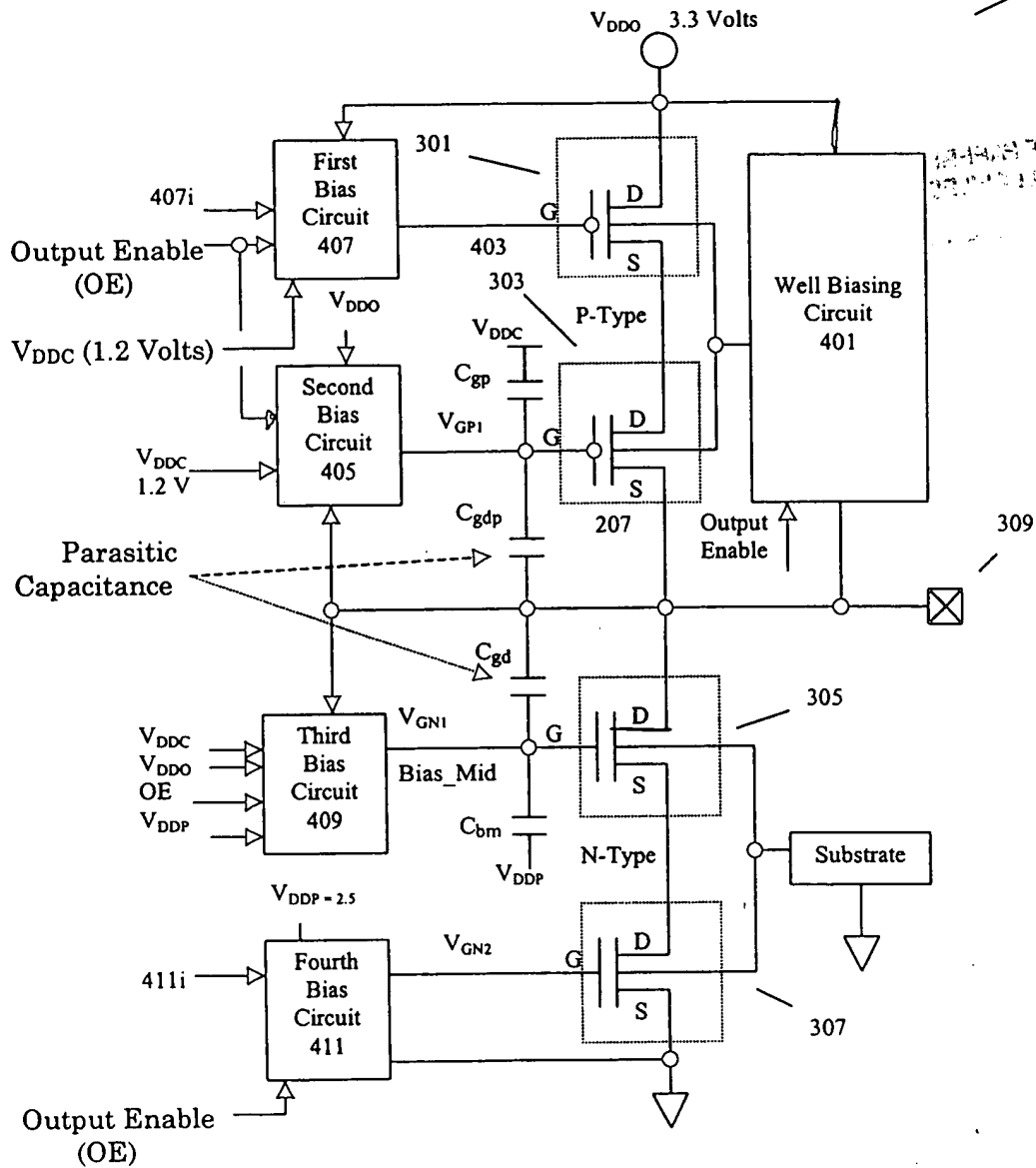


Figure 17

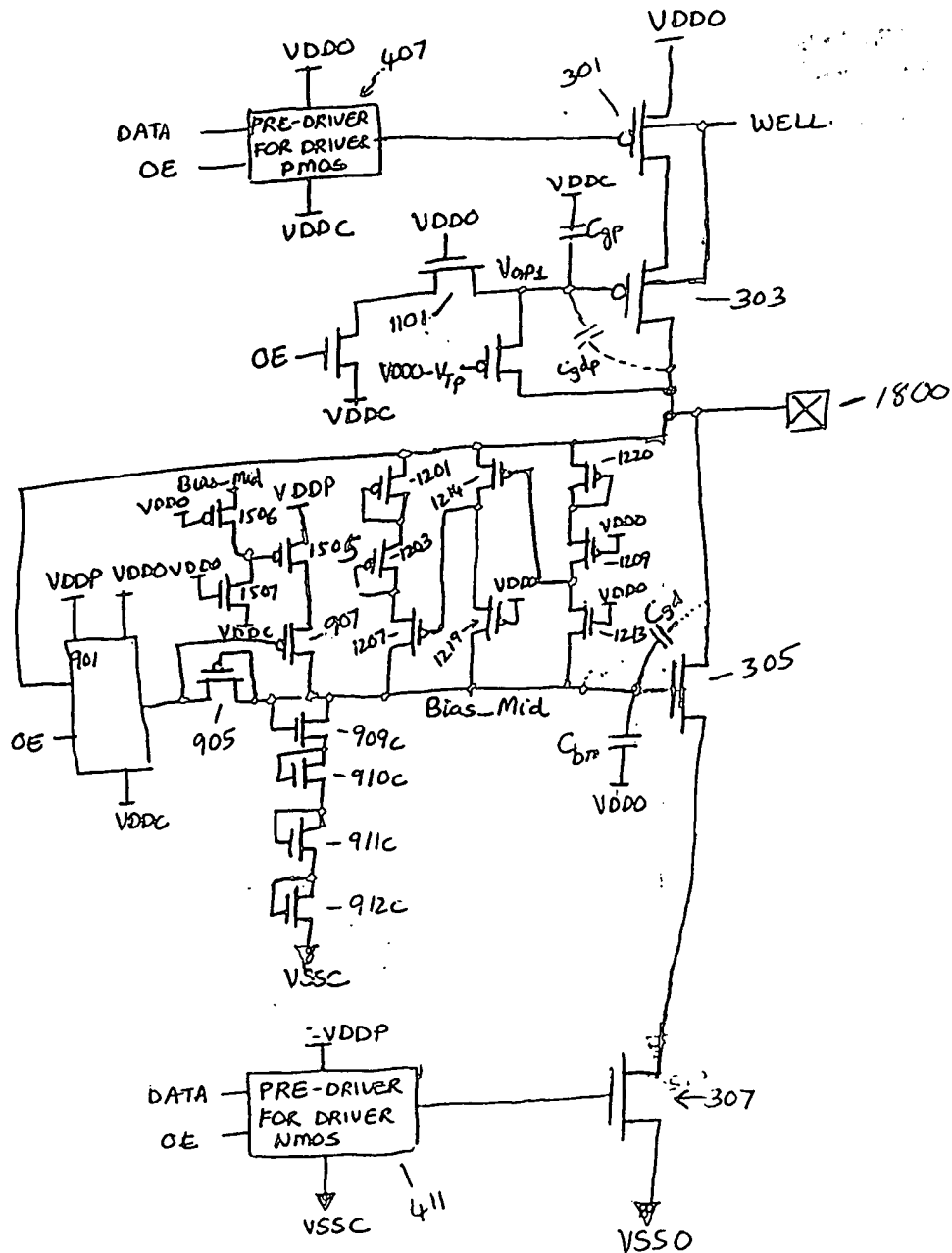


FIGURE 18